

Notice of References Cited	Application/Control No. 09/854,491	Applicant(s)/Patent Under Reexamination HOULIHANE ET AL.	
	Examiner Jason Proctor	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS					
*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
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FOREIGN PATENT DOCUMENTS						
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NON-PATENT DOCUMENTS		
*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Bob Holstine and Greg Haynes, "Developing an EDA Vendor-Independent ASIC Design System for VHDL", 1997, retrieved on 4 Oct 2006 from www.eetonline.com/editorial/1997/asicutmfeature9706.html , pages 1-7
	V	Synopsys, "SourceModel User's Manual for VHDL", 1999, pages 1-109
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.